

Fig. 1

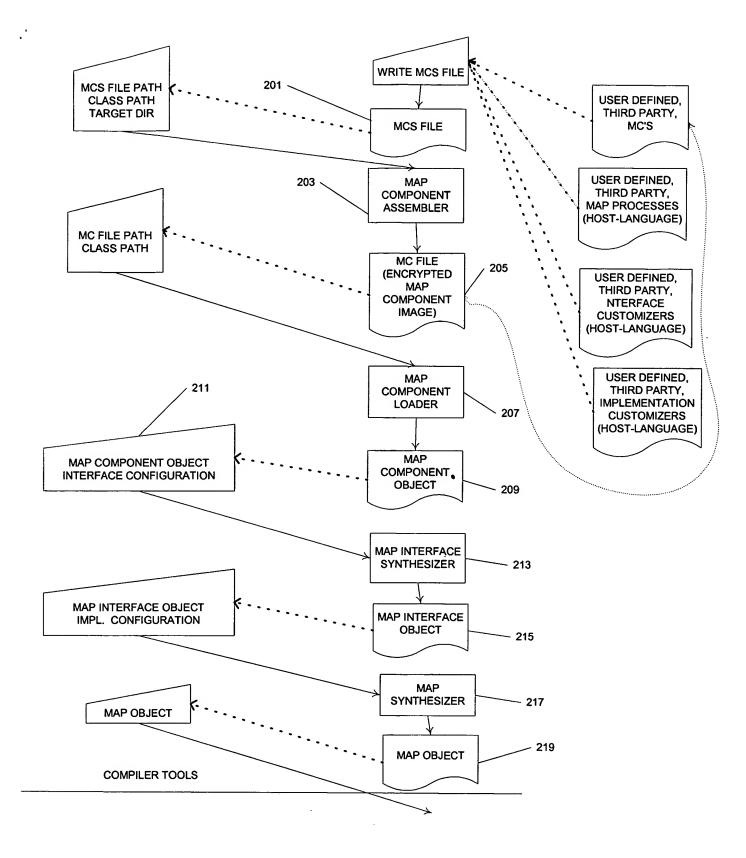


Fig. 2A

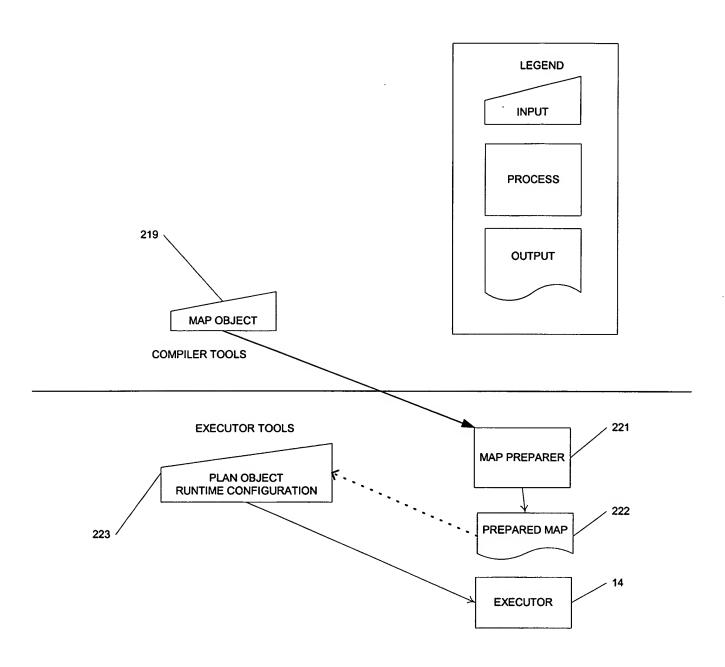
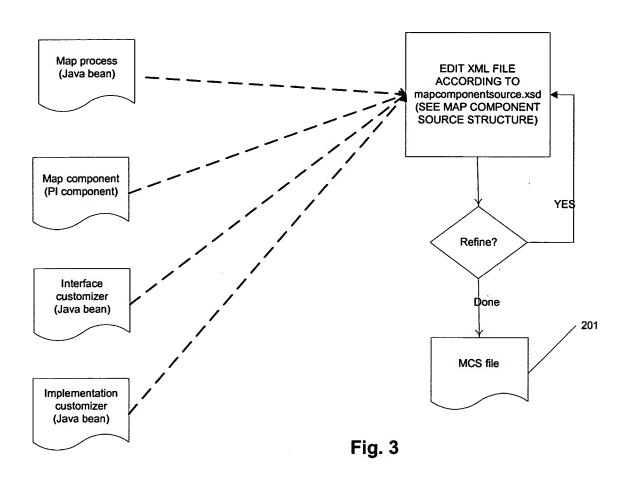


Fig. 2B

## MAP COMPONENT SOURCE CREATION



#### INTERFACE

#### **DOCUMENTATION**

INTERFACE PROPERTY LIST
IMPLEMENTATION PROPERTY LIST
RUNTIME PROPERTY LIST

PORT DECLARATION LIST ...

CUSTOMIZER DECLARATION AND CONFIGURATION

### **IMPLEMENTATION**

INTERNAL MAP COMPONENT INSTANCE DECLARATION LIST... (AND CONFIGURATIONS)

INTERNAL MAP PROCESS INSTANCE DECLARATION LIST... (AND CONFIGURATIONS)

PORT TO PORT LINK LIST...

CUSTOMIZER DECLARATION AND CONFIGURATION

## MAP COMPONENT ASSEMBLY PROCESS

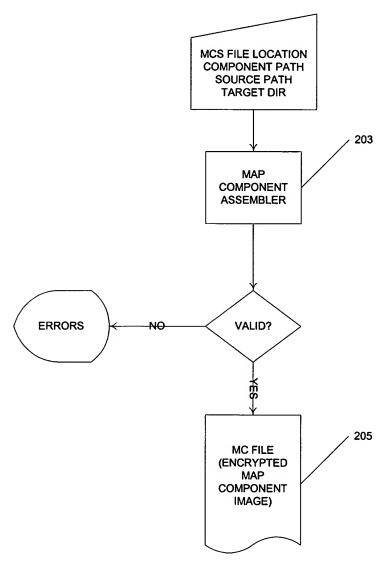


Fig. 5

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### **MAP COMPONENT**

#### **DOCUMENTATION**

INTERFACE PROPERTY LIST
IMPLEMENTATION PROPERTY LIST
RUNTIME PROPERTY LIST

**ENCRYPTED CONFIGURABLE SPECIFICATION** 

## MAP COMPONENT LOADING

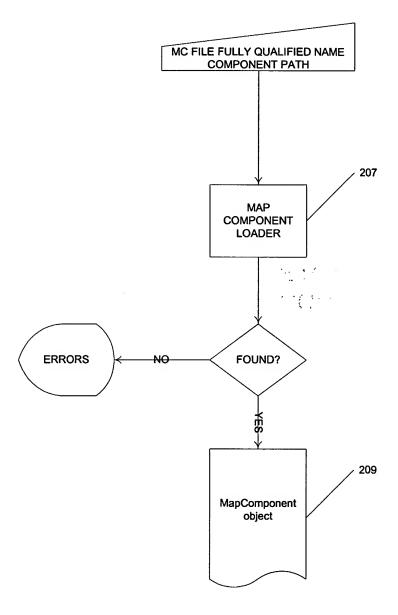


Fig. 7

## MAP INTERFACE SYNTHESIS

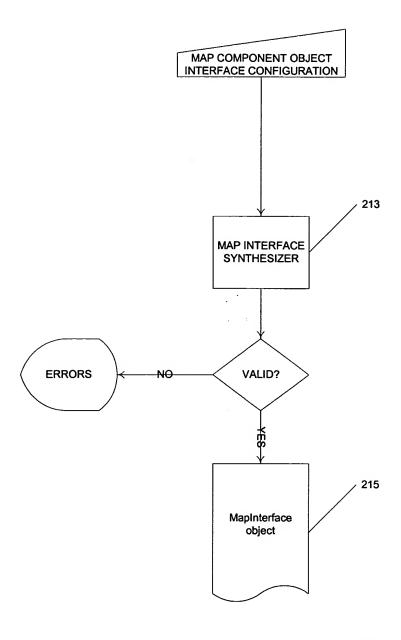


Fig. 8

### MAP INTERFACE

#### **DOCUMENTATION**

# INTERFACE CONFIGURATION IMPLEMENTATION PROPERTY LIST RUNTIME PROPERTY LIST

PORT LIST...

## MAP SYNTHESIS

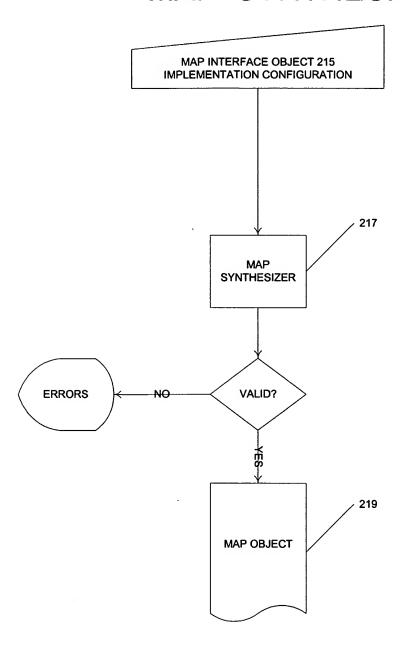


Fig. 10

MAP

#### **DOCUMENTATION**

# INTERFACE CONFIGURATION IMPLEMENTATION CONFIGURATION RUNTIME PROPERTY LIST

#### INTERFACE

PORT DECLARATION LIST ...

### IMPLEMENTATION (if open)

INTERNAL MAP COMPONENT INSTANCE DECLARATION LIST... (AND CONFIGURATIONS)

INTERNAL MAP PROCESS INSTANCE DECLARATION LIST... (AND CONFIGURATIONS)

PORT TO PORT LINK LIST...

**INTERNAL MAP LIST...** 

INPUT PORT LIST	
PUT PORTS OUTPUT PORT OUTPUT PORT TYPE INPUT PORT READER LIST	

MAP PROCESS LIST ...

## MAP PREPARATION

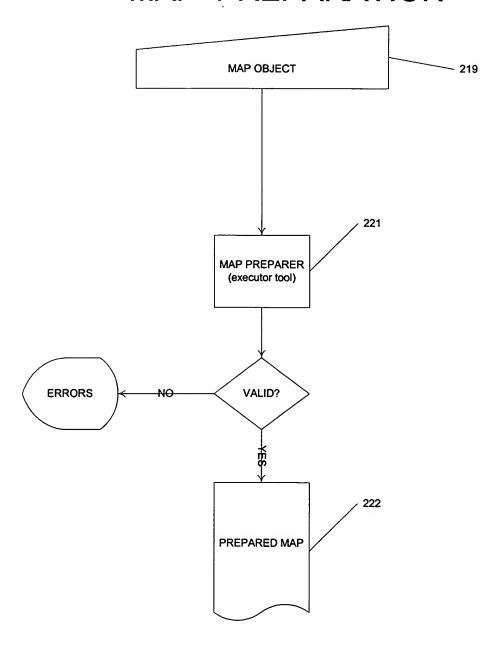


Fig. 13

## PREPARED MAP EXECUTION

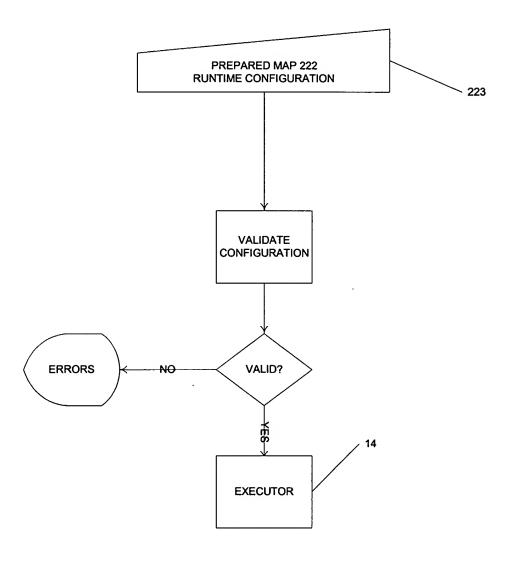
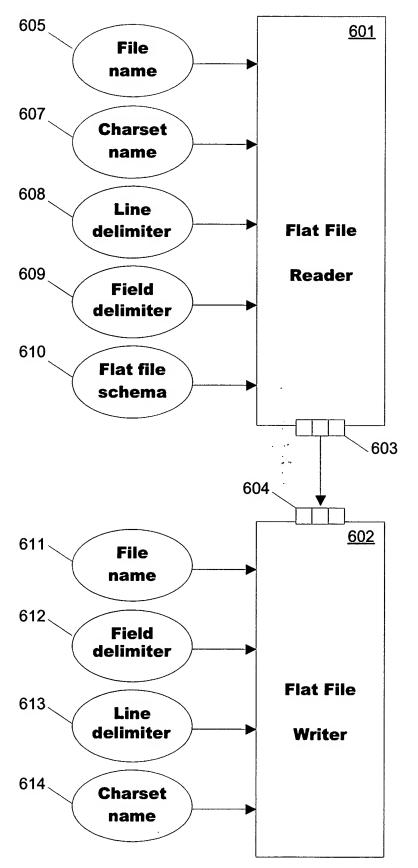
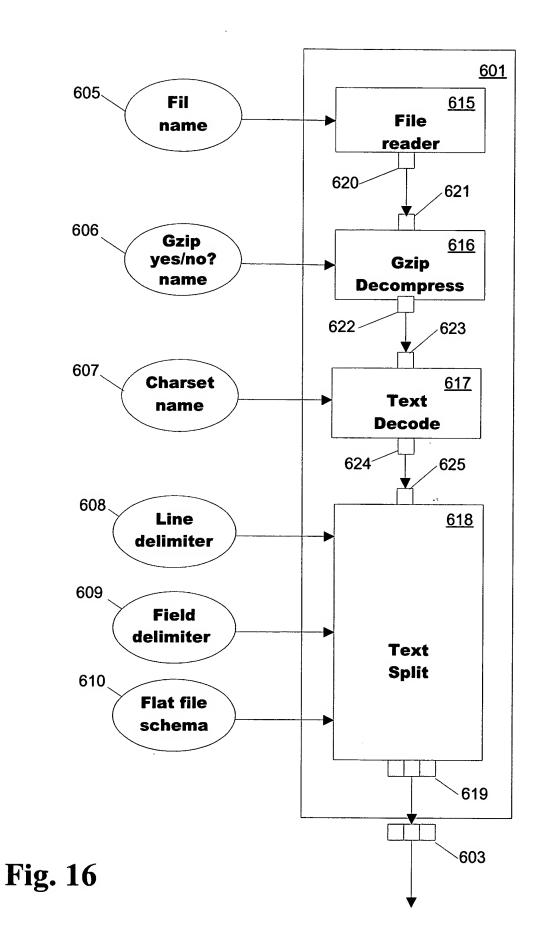
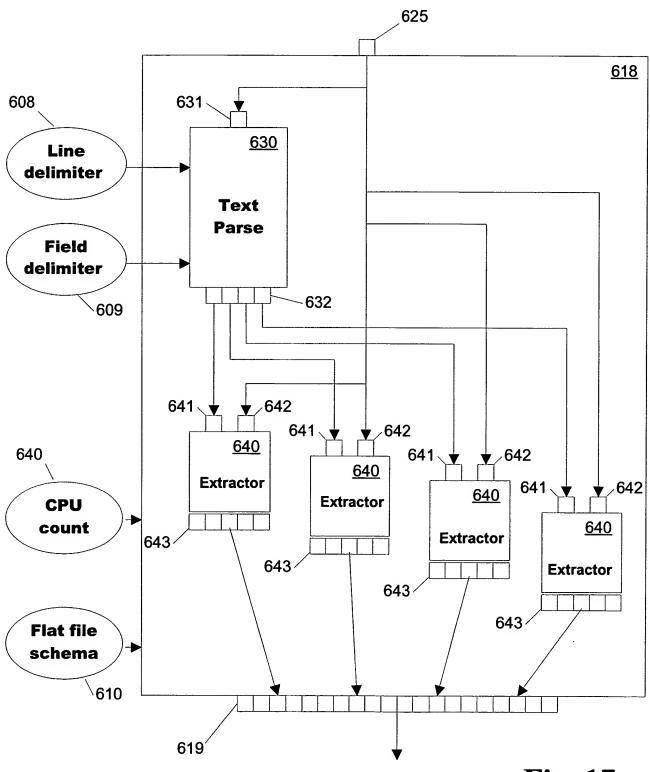


Fig. 14

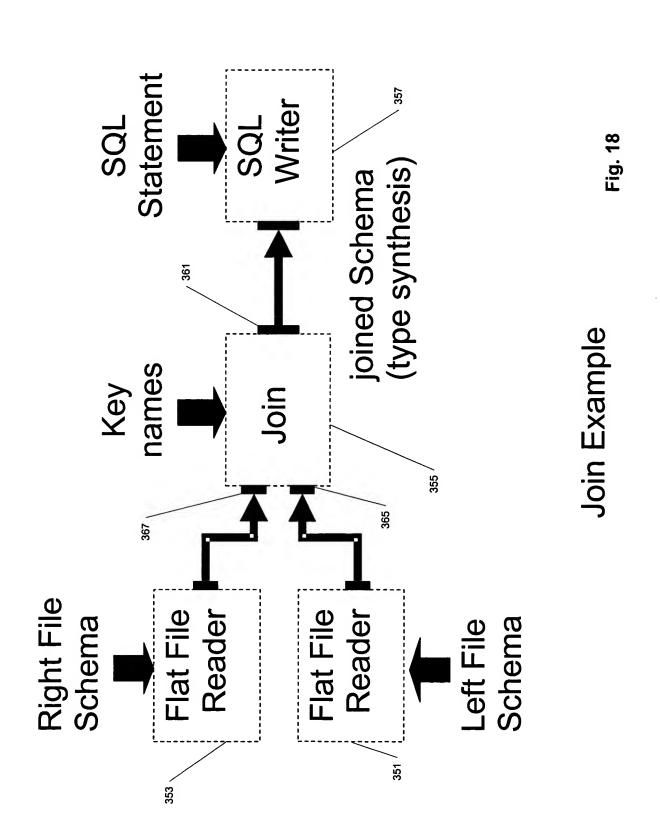


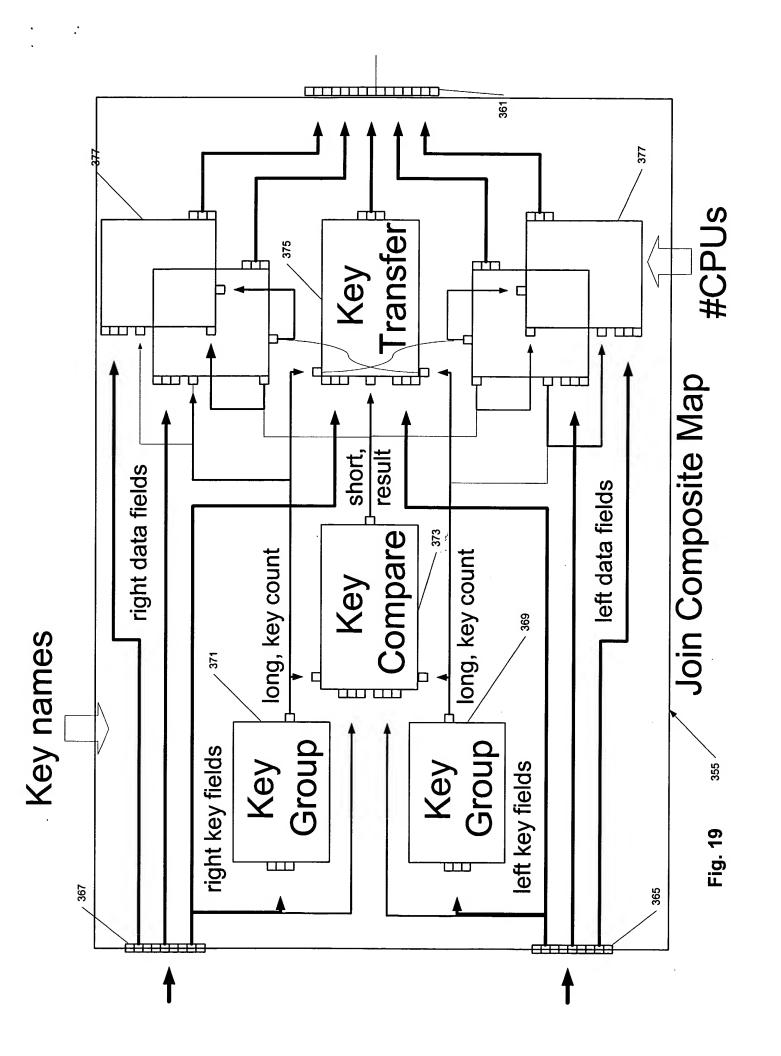
**Fig. 15** 

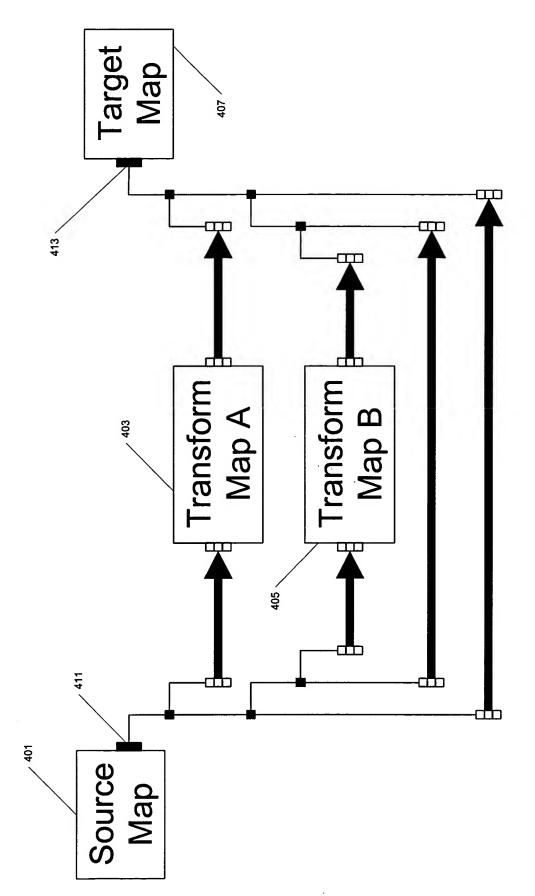




**Fig. 17** 







Hierarchical Composite Ports

Fig. 20

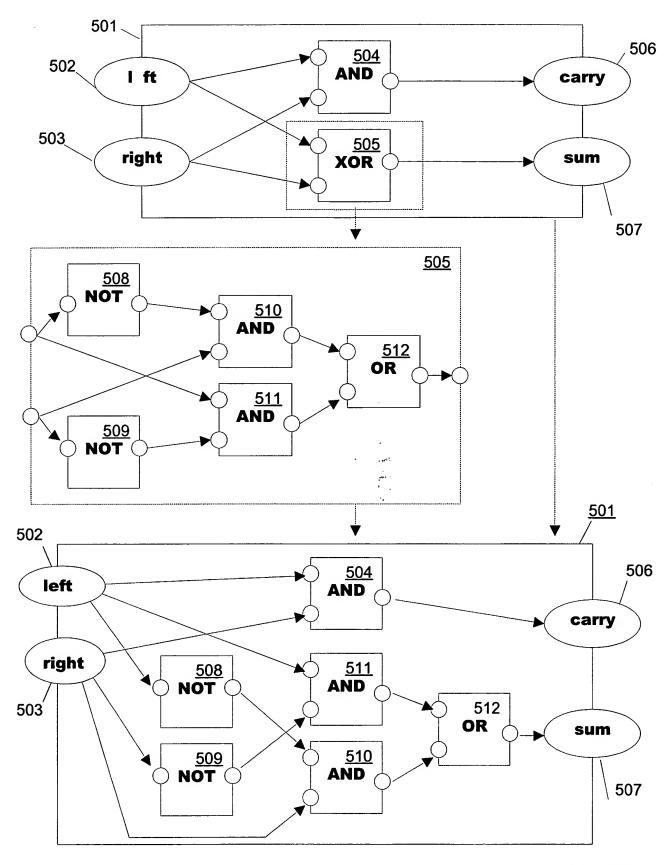


Fig. 21

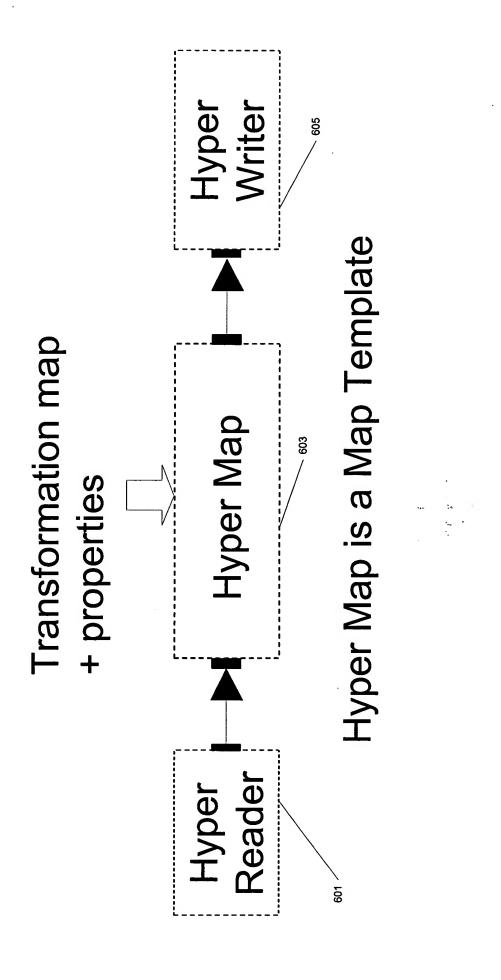
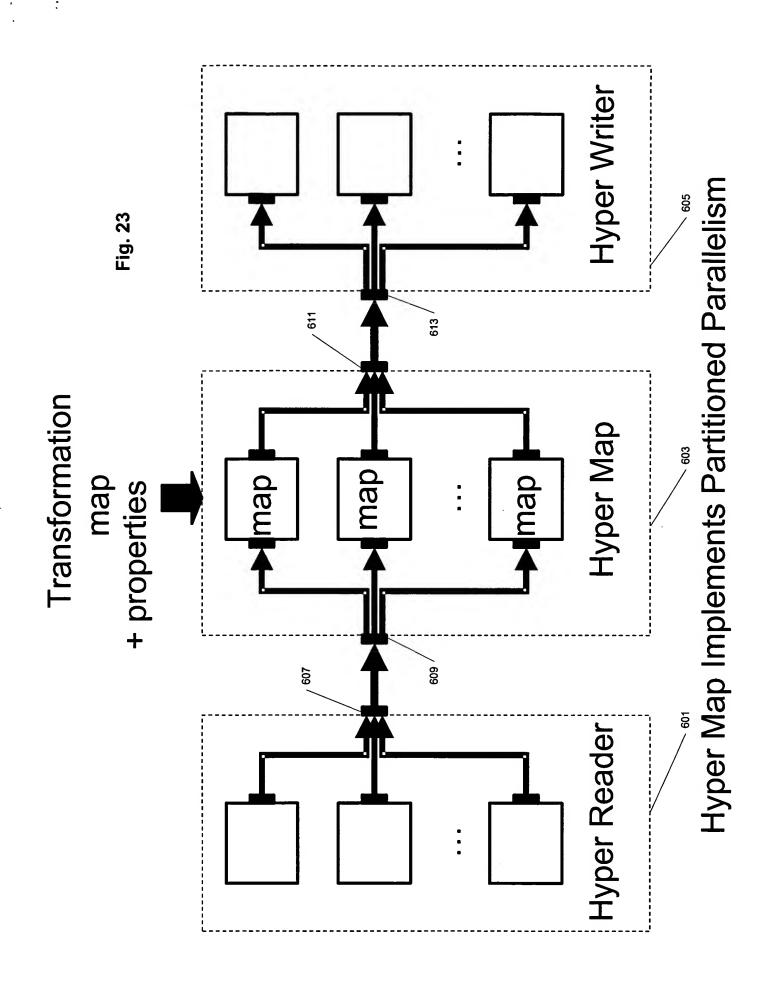
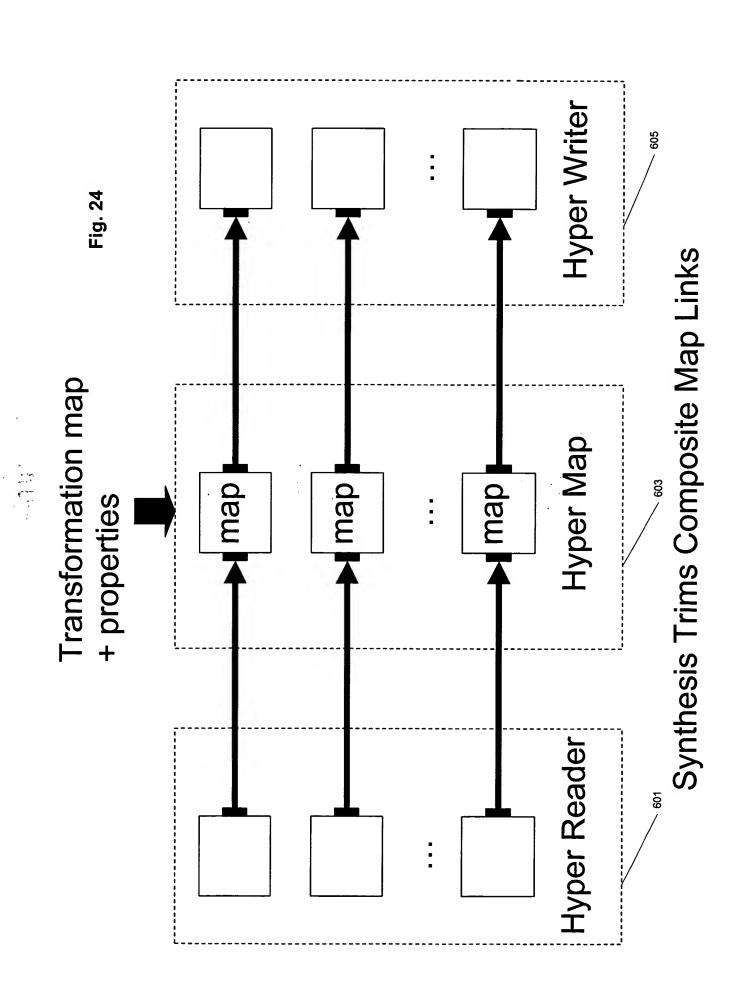
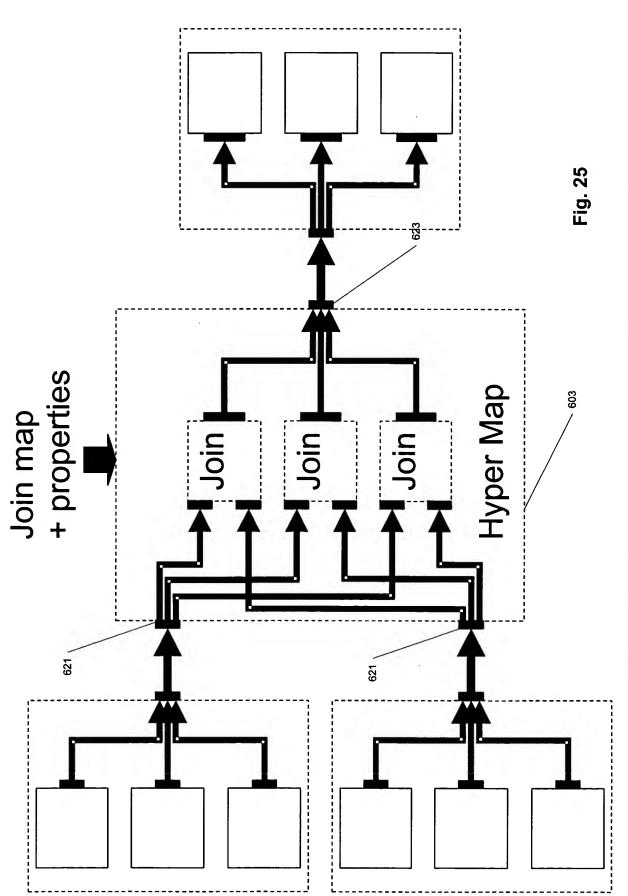


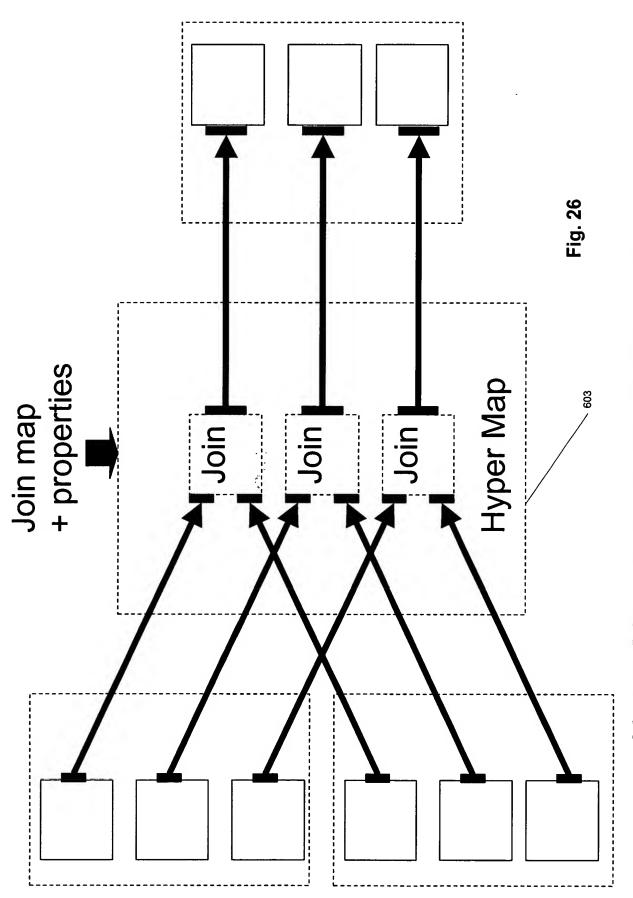
Fig. 22







Hyper Map Partitions another Map's Interface



Hyper Map Implements Partitioned Join

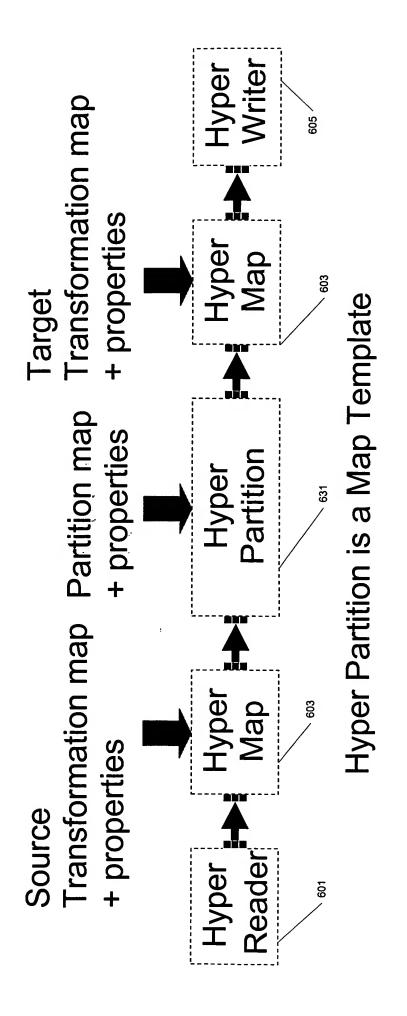
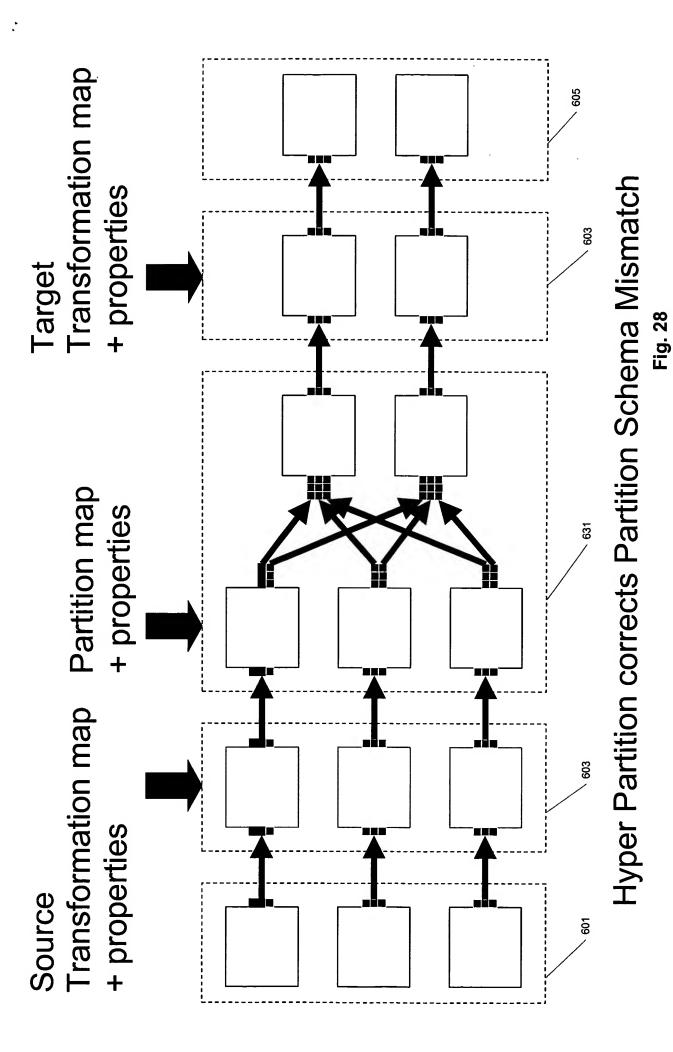
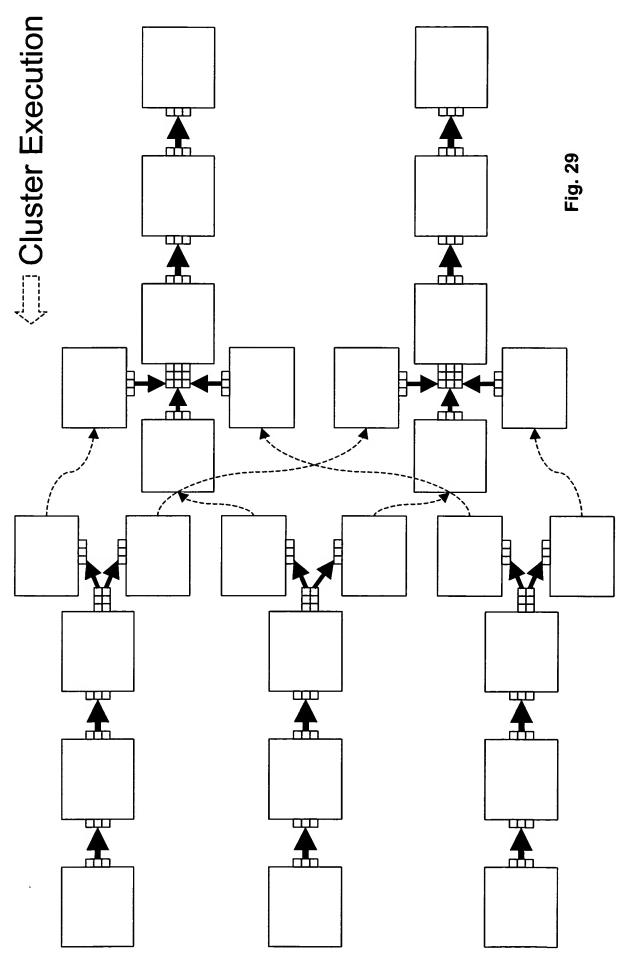


Fig. 27





Cluster Execution of Hyper Partition Pattern

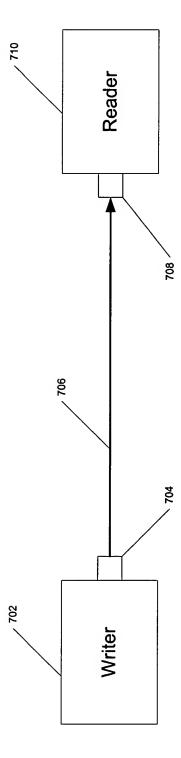


Fig. 30

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